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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10062595	FILING DATE 01/31/2002	CLASS 072	SUBCLASS 456	GAU 3725	EXAMINER Jones
**APPLICANTS: Siegel Harry; Chiu Anthony;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged: Examiners's initials				ATTORNEY DOCKET NO 01-C-055 (STMI01-01055)	
TITLE : System and method for aligning an integrated circuit die on an integrated circuit substrate					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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